

Electronic Patent Application Fee Transmittal

Application Number:	10710602			
Filing Date:	23-Jul-2004			
Title of Invention:	Method to selectively correct critical dimension errors in the semiconductor industry			
First Named Inventor/Applicant Name:	Jed H. Rankin			
Filer:	Jack P. Friedman/Kimberly Dwileski			
Attorney Docket Number:	BUR920040086US1			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	790	790
Total in USD (\$)				790